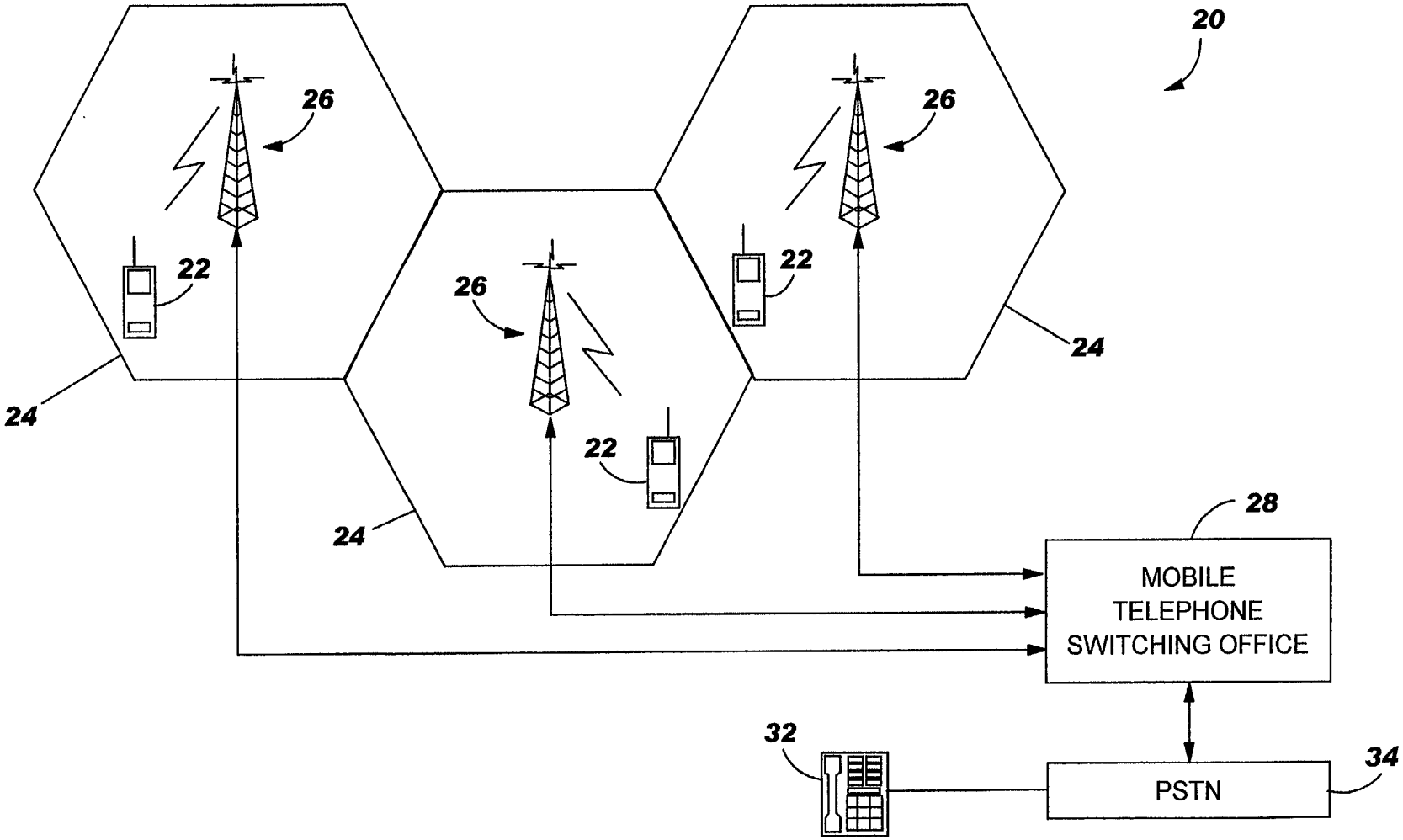
TOETZO" TO92E860

FIGURE 1



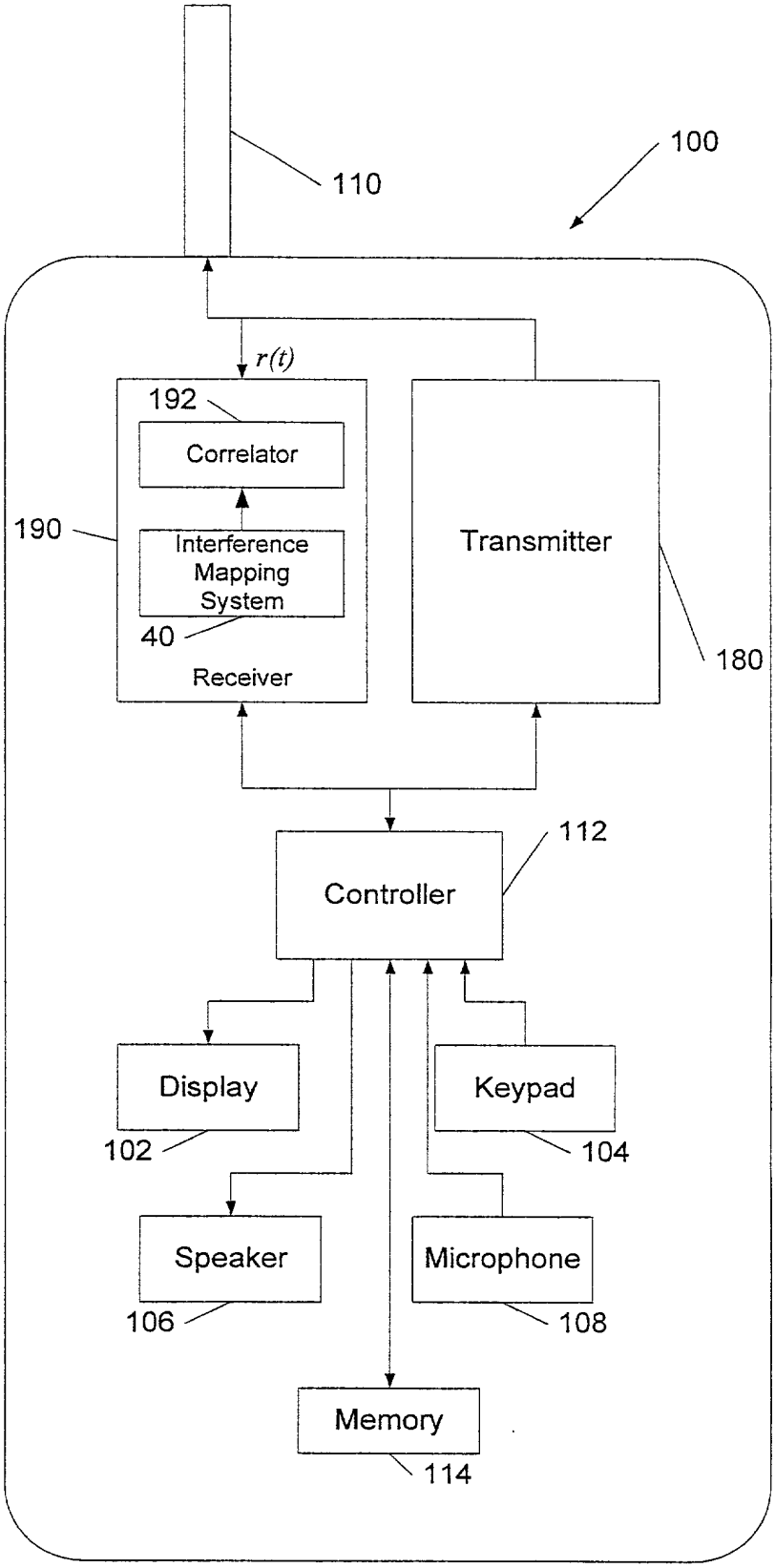

110
100
r(t)
192
Correlator
190
Interference Mapping System
40
Receiver
Transmitter
180
112
Controller
Display
102
Keypad
104
Speaker
106
Microphone
108
Memory
114

Figure 2

TOET/01 T092860

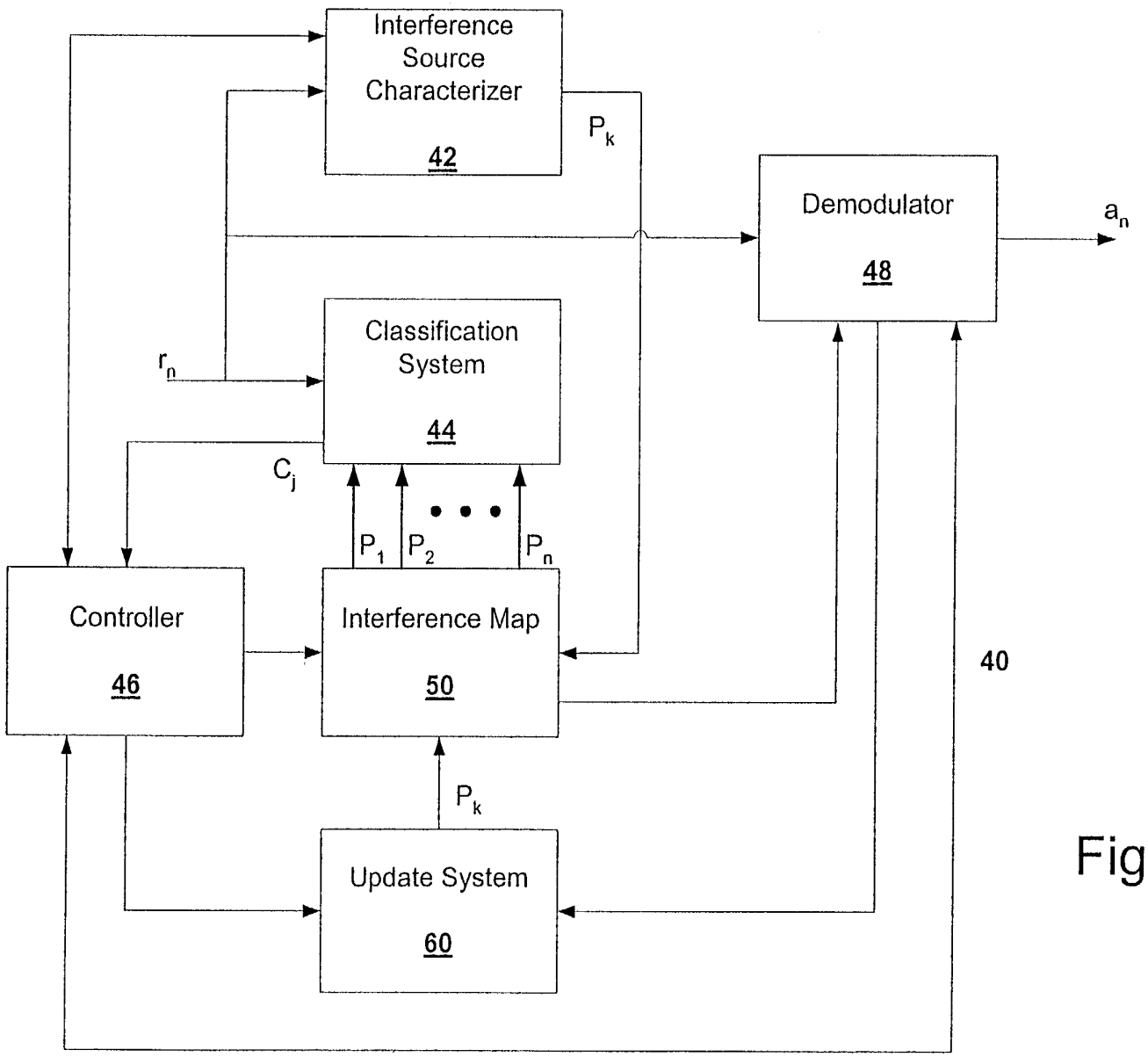


Figure 3

FOR OFFICIAL USE ONLY

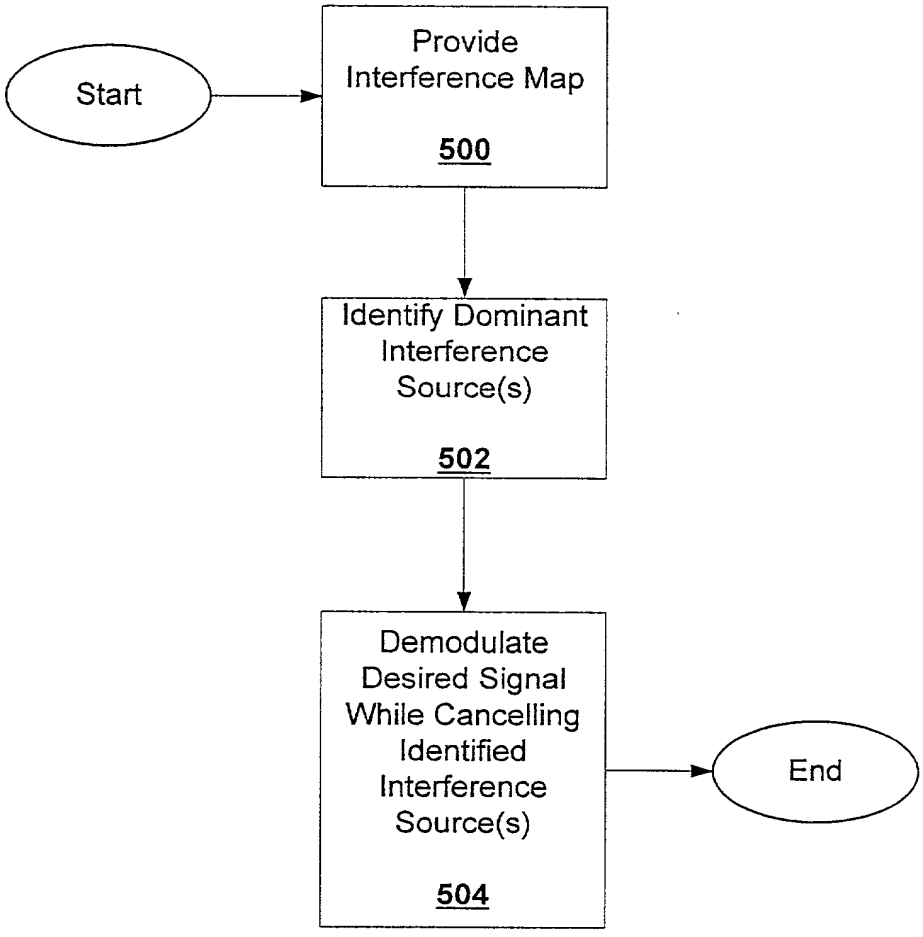


Figure 4

TOETZO" EP926860

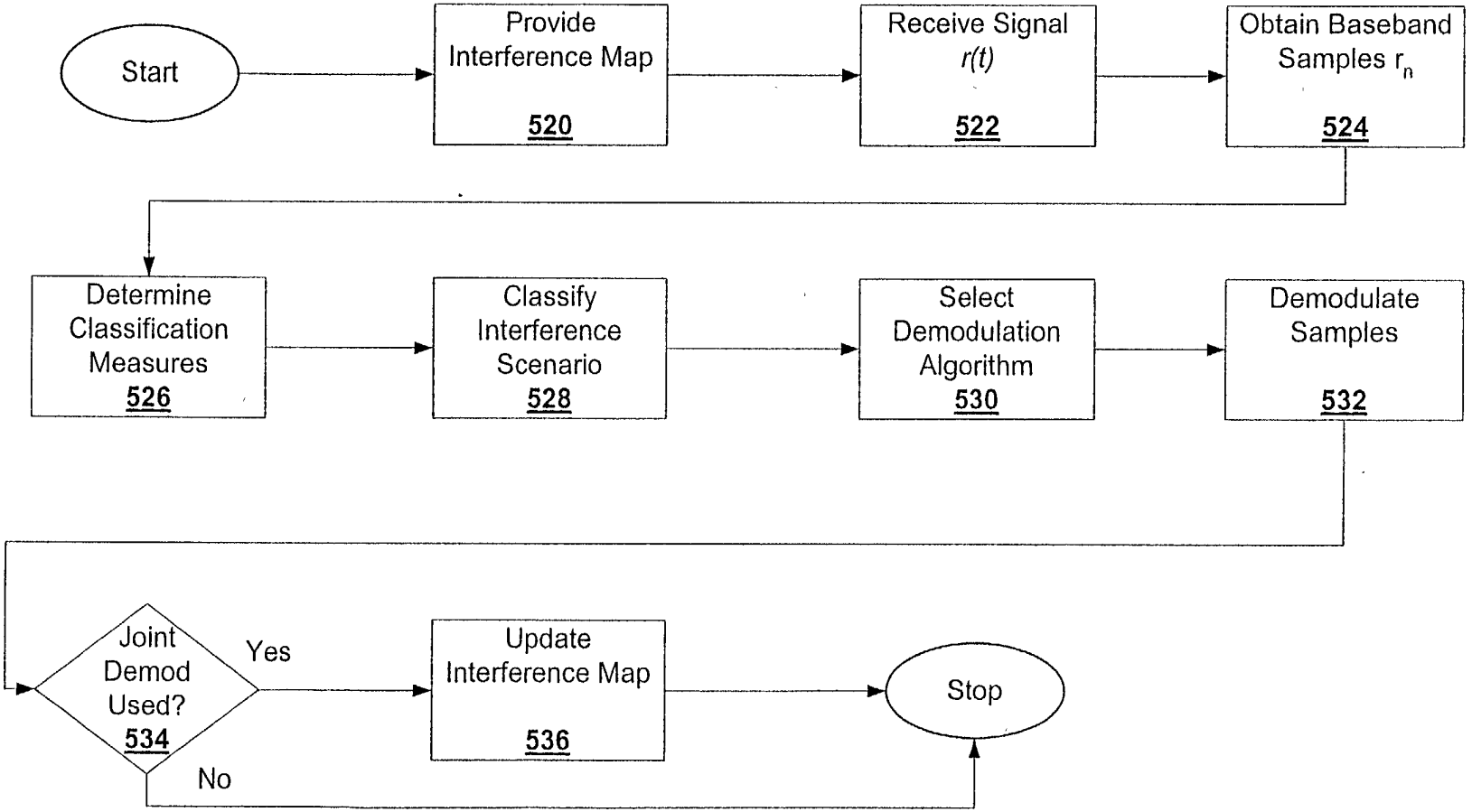
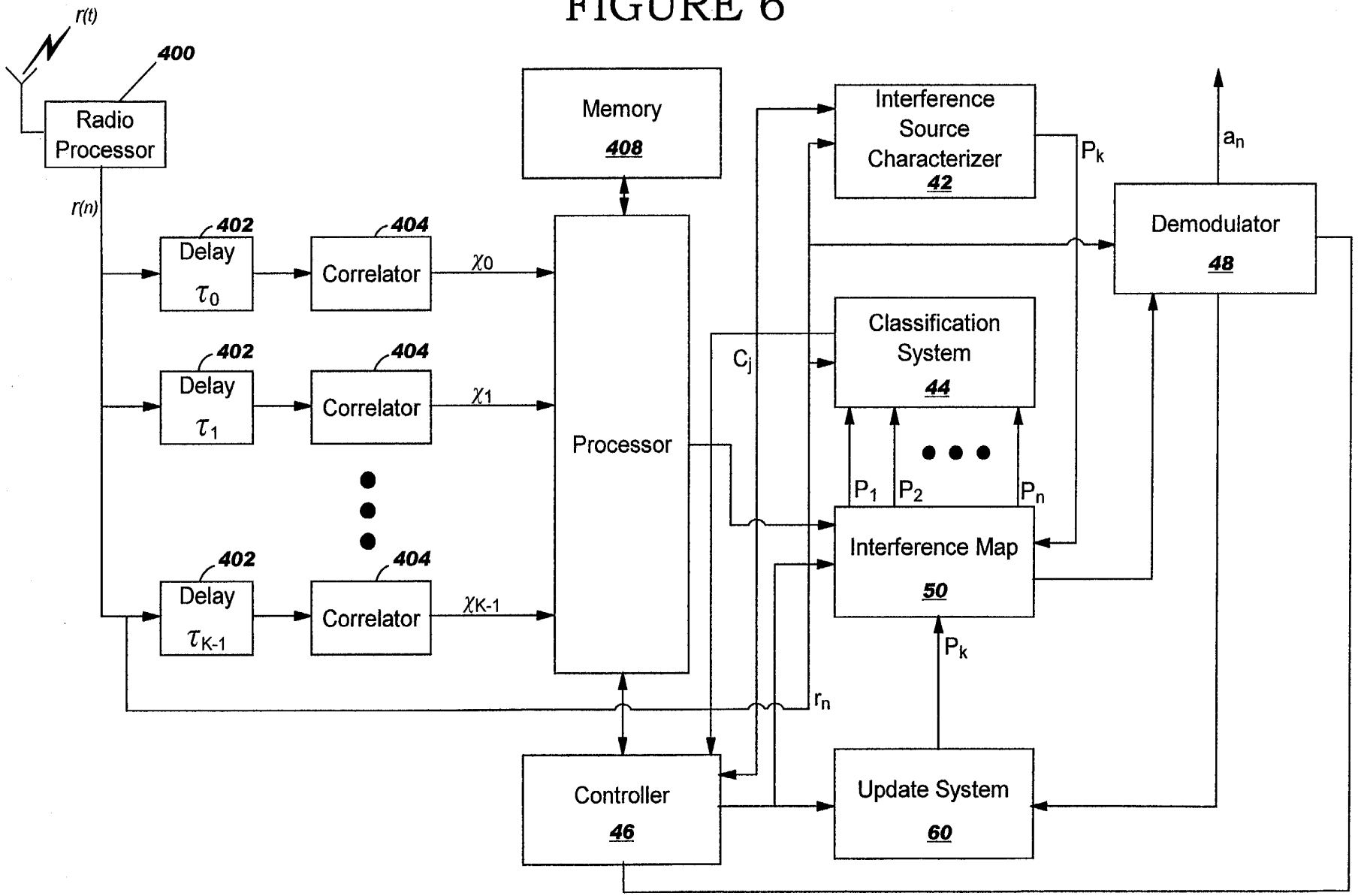


Figure 5

TOETZOT T092E86D

FIGURE 6



AN EXEMPLARY INTERFERENCE MAP STRUCTURE

	SLOT TIMING OFFSET	FREQUENCY OFFSET	SYNC PATTERN	DVCC
INTERFERING SOURCE I	-29 3/8 symbols	58 Hz	2	10100100
INTERFERING SOURCE 2	+113 1/4 symbols	87Hz	5	01011110
INTERFERING SOURCE 3	+12 3/4 symbols	-25Hz	6	01011011
INTERFERING SOURCE 4	-22 7/8 symbols	-125Hz	1	11101110
INTERFERING SOURCE 5	-113 1/2 symbols	117Hz	4	10011000
INTERFERING SOURCE 6	+47 5/8 symbols	-11Hz	4	10111110

FIGURE 7